

100V N-Channel Enhancement Mode MOSFET

Description

The 30N10 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

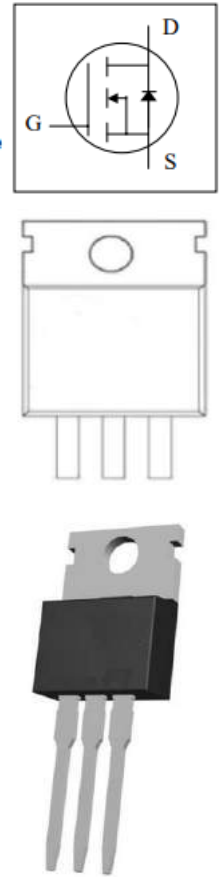
General Features

$V_{DS} = 100V$ $I_D = 30A$

$R_{DS(ON)} < 40m\Omega$ @ $V_{GS}=10V$

Application

Battery protection
 Load switch
 Uninterruptible power supply



Absolute Maximum Ratings ($T_C=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	100	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D@T_C=25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^1$	30	A
$I_D@T_C=100^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^1$	26	A
I_{DM}	Pulsed Drain Current ²	72	A
EAS	Single Pulse Avalanche Energy ³	126	mJ
I_{AS}	Avalanche Current	13	A
$P_D@T_C=25^\circ C$	Total Power Dissipation ⁴	125	W
T_{STG}	Storage Temperature Range	-55 to 175	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 175	$^\circ C$

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R _{θJA}	Thermal Resistance Junction-ambient ¹	62	°C/W
R _{θJC}	Thermal Resistance Junction-Case ¹	1.2	°C/W

Electrical Characteristics (T_J=25°C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	100	---	---	V
ΔBV _{DSS} /ΔT _J	BVDSS Temperature Coefficient	Reference to 25°C, I _D =1mA	---	0.098	---	V/°C
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =10V, I _D =16A	---	36	40	mΩ
		V _{GS} =4.5V, I _D =10A	---	---	50	
V _{GS(th)}	Gate Threshold Voltage		1.5	---	2.5	V
ΔV _{GS(th)}	V _{GS(th)} Temperature Coefficient	V _{GS} =V _{DS} , I _D =250uA	---	-5.52	---	mV/°C
I _{DSS}	Drain-Source Leakage Current	V _{DS} =80V, V _{GS} =0V, T _J =25°C	---	---	10	uA
		V _{DS} =80V, V _{GS} =0V, T _J =55°C	---	---	100	
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V, V _{DS} =0V	---	---	±100	nA
g _{fs}	Forward Transconductance	V _{DS} =5V, I _D =16A	---	30	---	S
R _g	Gate Resistance	V _{DS} =0V, V _{GS} =0V, f=1MHz	---	1.6	---	
Q _g	Total Gate Charge (10V)		---	45.6	---	nC
Q _{gs}	Gate-Source Charge	V _{DS} =80V, V _{GS} =10V, I _D =16A	---	6.7	---	
Q _{gd}	Gate-Drain Charge		---	11.8	---	
T _{d(on)}	Turn-On Delay Time		---	12	---	ns
T _r	Rise Time	V _{DD} =50V, V _{GS} =10V, R _G =3.3	---	32.2	---	
T _{d(off)}	Turn-Off Delay Time	I _D =10A	---	42	---	
T _f	Fall Time		---	13.4	---	
C _{iss}	Input Capacitance		---	2270	---	pF
C _{oss}	Output Capacitance	V _{DS} =25V, V _{GS} =0V, f=1MHz	---	130	---	
C _{rss}	Reverse Transfer Capacitance		---	90	---	
I _S	Continuous Source Current ^{1,5}	V _G =V _D =0V, Force Current	---	---	36	A
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V, I _S =1A, T _J =25°C	---	---	1.2	V
t _{rr}	Reverse Recovery Time	I _F =16A, dI/dt=100A/μs, T _J =25°C	---	33	---	nS
Q _{rr}	Reverse Recovery Charge		---	28	---	nC

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Typical Characteristics

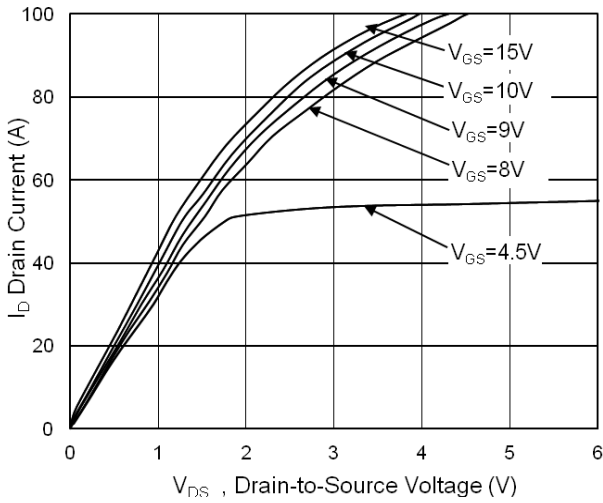


Fig.1 Typical Output Characteristics

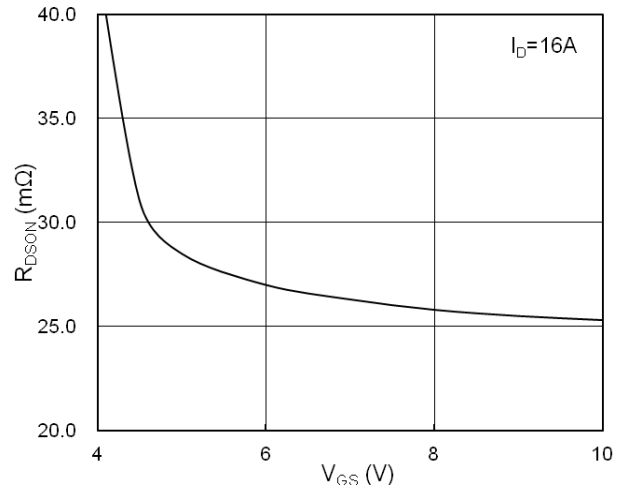


Fig.2 On-Resistance vs. G-S Voltage

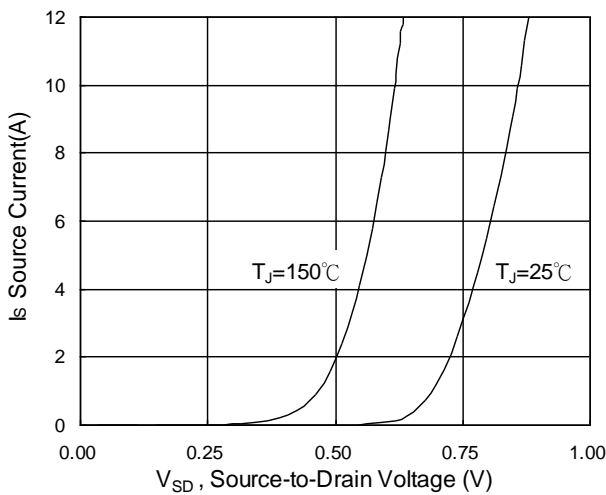


Fig.3 Source Drain Forward Characteristics

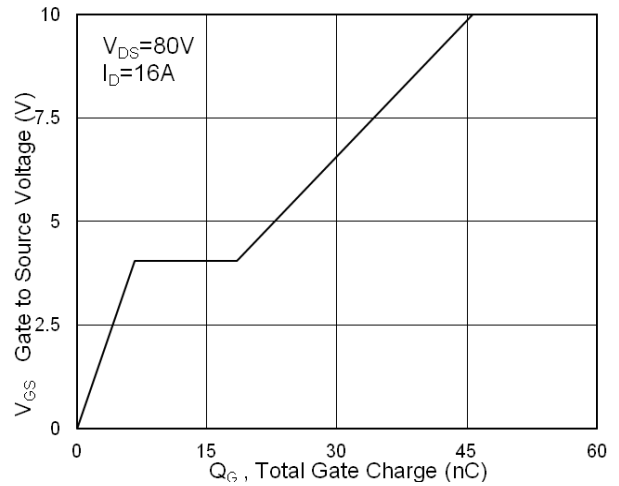


Fig.4 Gate-Charge Characteristics

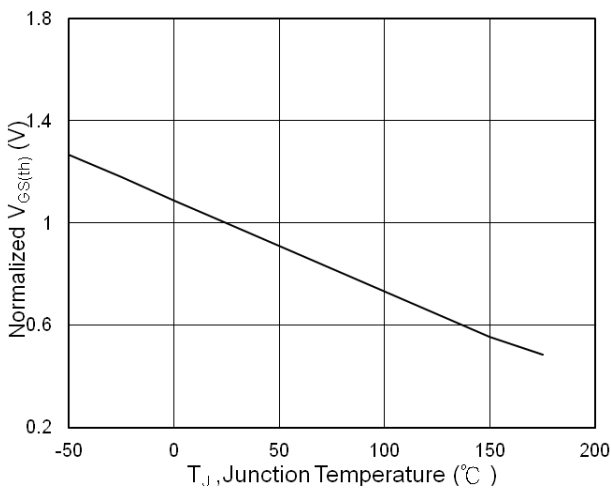


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

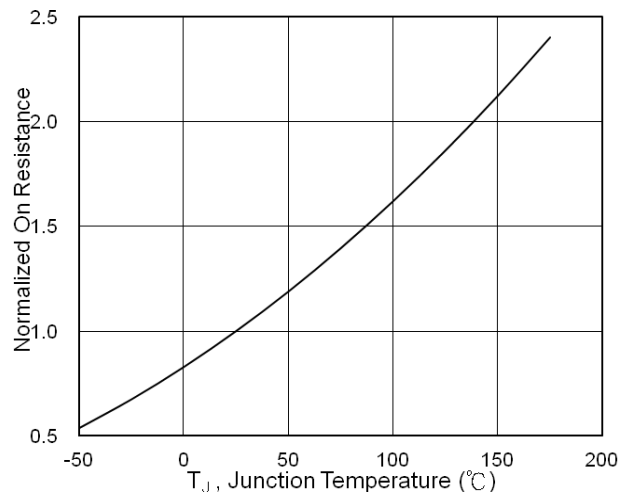


Fig.6 Normalized $R_{DS(on)}$ vs. T_J

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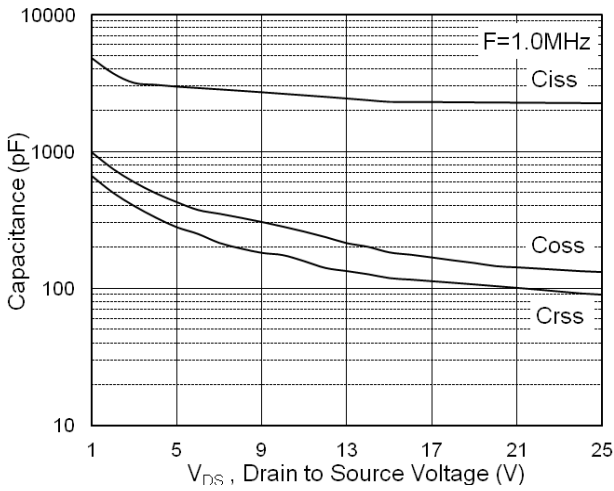


Fig.7 Capacitance

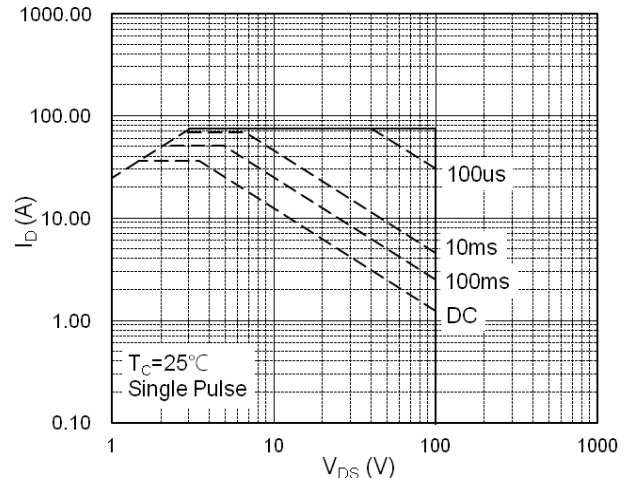


Fig.8 Safe Operating Area

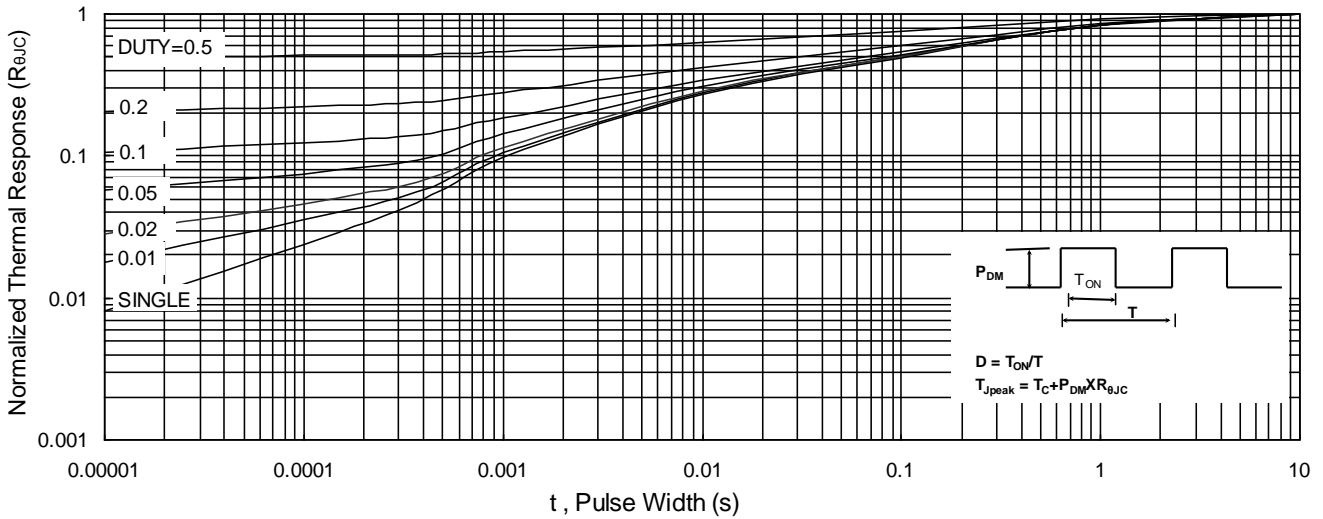


Fig.9 Normalized Maximum Transient Thermal Impedance

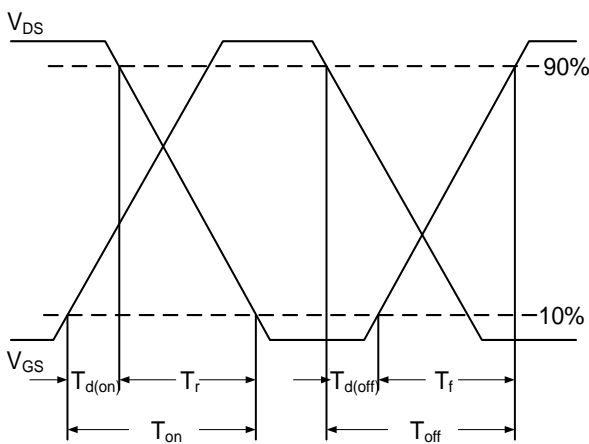


Fig.10 Switching Time Waveform

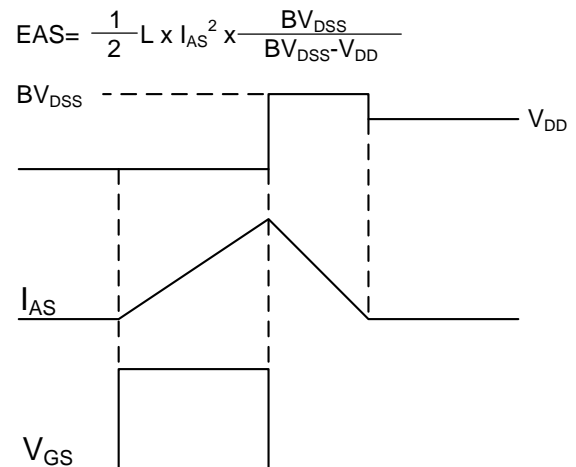


Fig.11 Unclamped Inductive Switching Waveform

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